

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

O.I.P.E.		PATENT DATE
SCANNED <i>1/15/1</i>	Q.A. <i>TC</i>	

APPLICATION NO. 09/656106	CONT/PRIOR F	CLASS 361 310	SUBCLASS 320	ART UNIT 2041 2834	EXAMINER Gonzalez
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APPLICANTS

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Chip electronic components and mounting structure for the same

TITLE

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	

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